

FIG. 1

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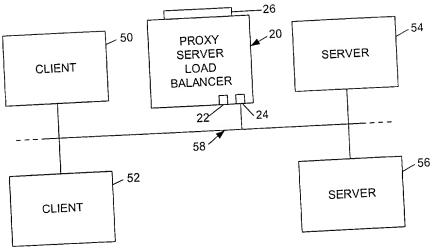


FIG. 2

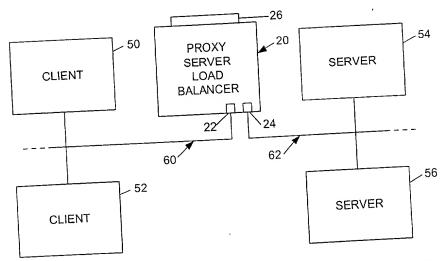


FIG. 3

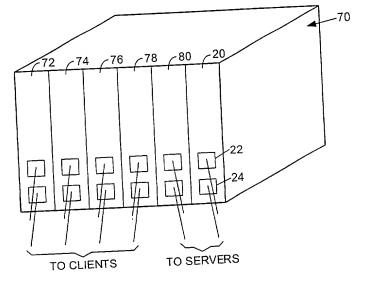


FIG. 4

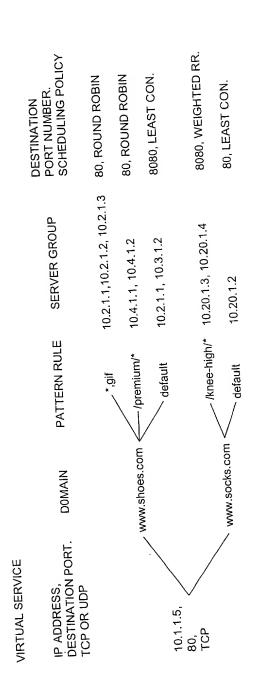


FIG. 5

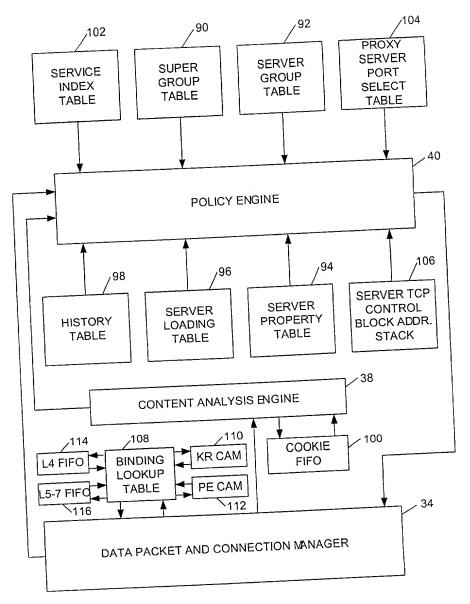
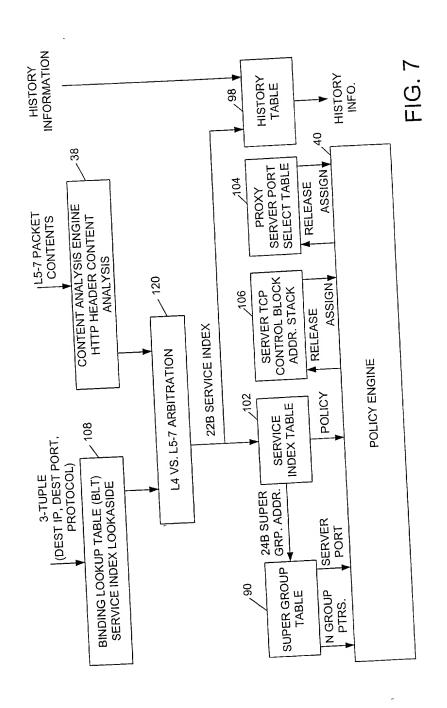
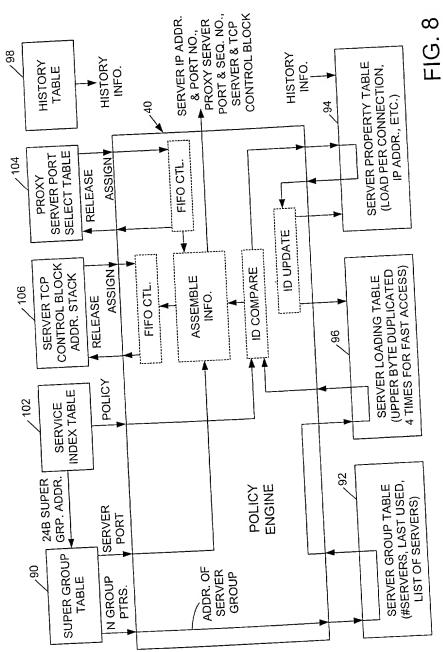


FIG. 6

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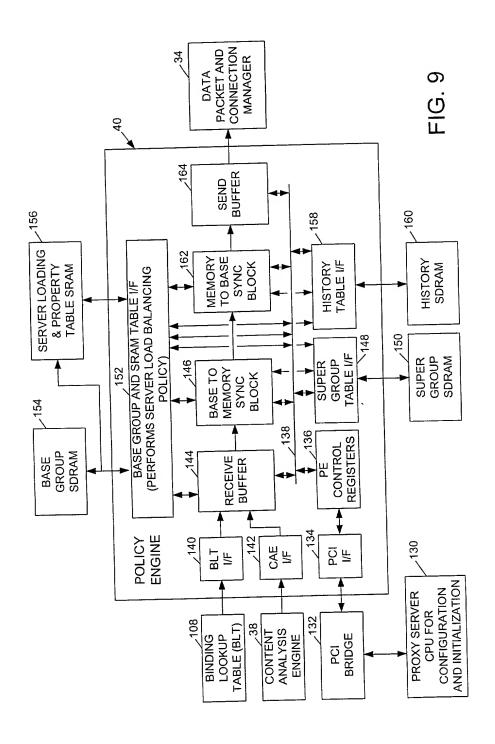


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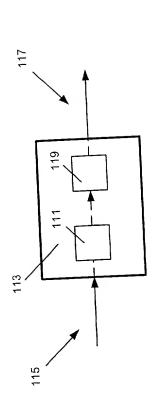


FIGURE 10A

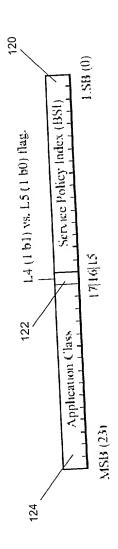


FIGURE 10B

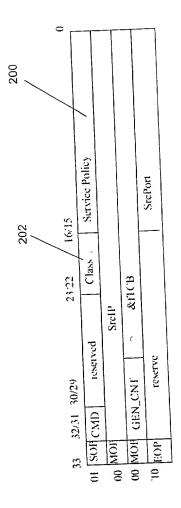


FIGURE 10C

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		Reserved	Chent_IP_A	 Reserved	-	cookie hashing valid	server_id_valid	Reserved	Keserved

FIGURE 10D

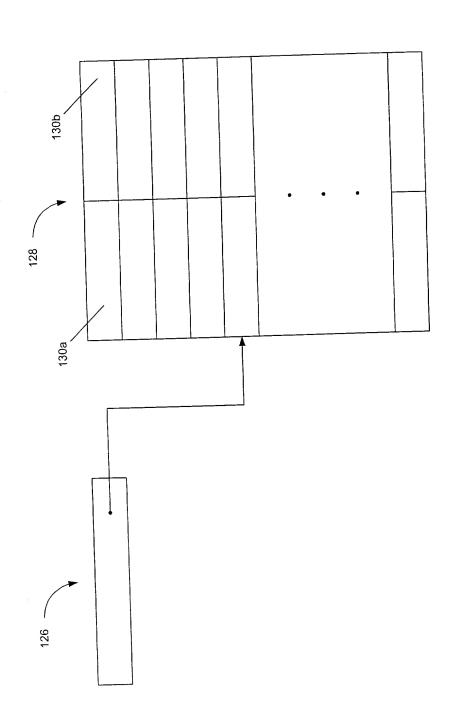
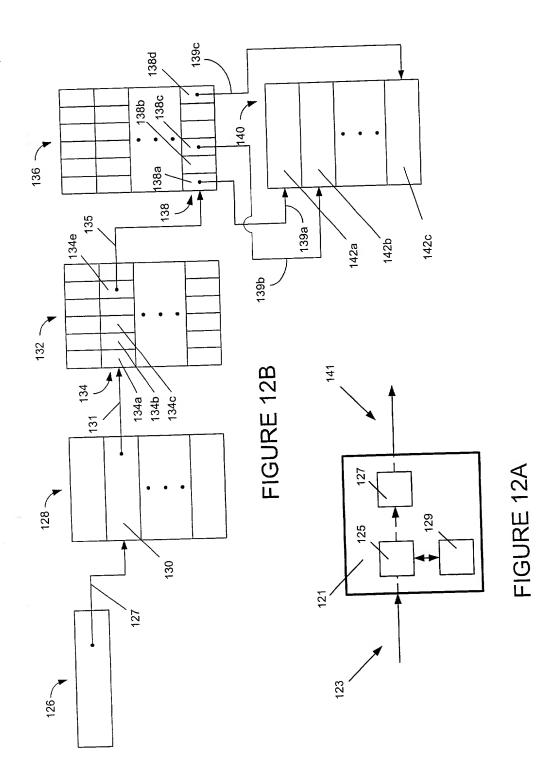


FIGURE 11

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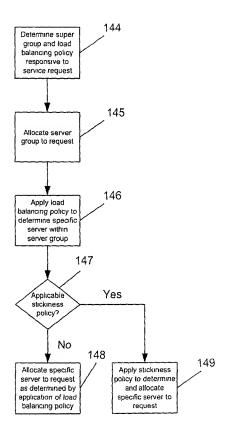


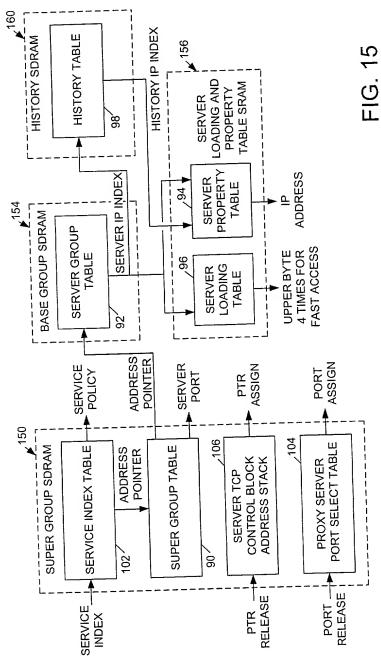
FIGURE 13

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FIGURE 14

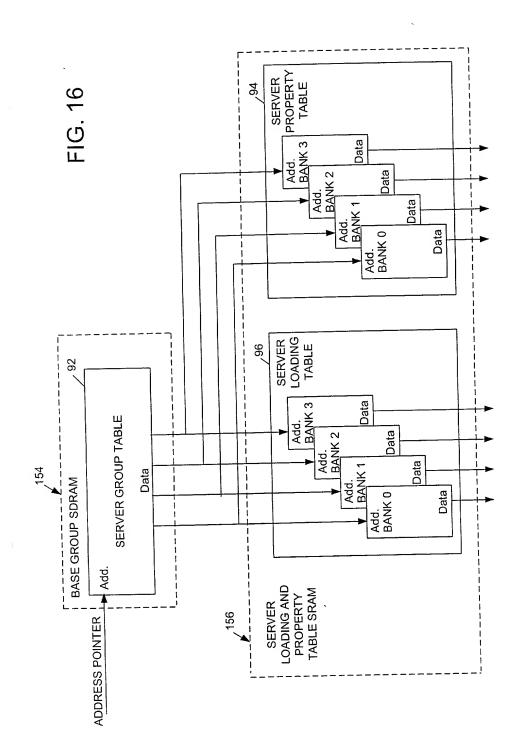
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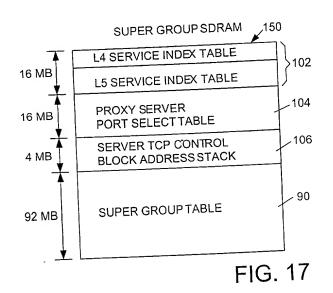
· #1

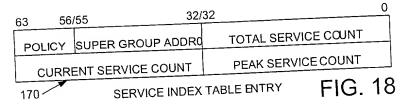


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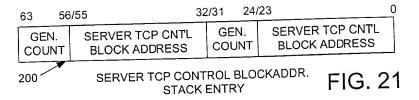


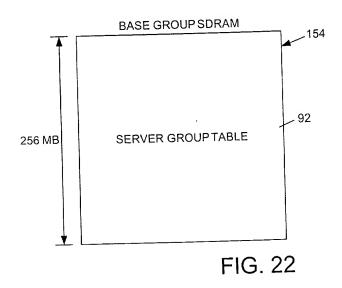


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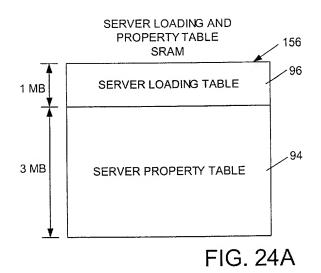
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128K	128K X 18 SRAM	128K X 18 SRAM	128K X 18 SRAM	128K X 18 SRAM	ROW 3				
128K	128K X 18 SRAM	128K X 18 SRAM	128K X 18 SRAM	128K X 18 SRAM	ROW 4				
	COL. 4	COL. 3	COL. 2	COL. 1					
					~ 4 F				

FIG. 24B

